

Attorney's Docket No. 042390.P9774

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2127

PATENT

03-11-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

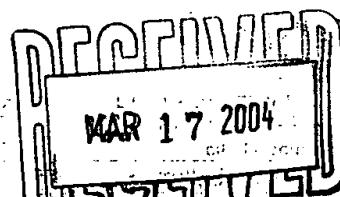
In re Application of:)
Erik Cota-Robles, et al.)
Application No.: 09/823,482)
Filed: March 30, 2001)
For: METHOD AND APPARATUS FOR)
CONSTRUCTING HOST PROCESSOR SOFT)
DEVICES INDEPENDENT OF THE HOST)
PROCESSOR OPERATING SYSTEM)

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Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with enclosed is a copy of the documents cited on that form. It is respectfully requested that the cited copies of the documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

FIRST CLASS CERTIFICATE OF MAILING

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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(Use as many sheets as necessary)</p> <p style="text-align: center;">  OIEE MAR 11 2004 JC26 <small>PATENT & TRADEMARK OFFICE</small> </p>											
<p>Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.</p> <p>Complete if known</p> <table border="1"> <tr> <td>Application Number</td> <td>09/823,482</td> </tr> <tr> <td>Filing Date</td> <td>March 30, 2001</td> </tr> <tr> <td>First Named Inventor</td> <td>Cota-Robles, Erik</td> </tr> <tr> <td>Group Art Unit</td> <td>2127</td> </tr> <tr> <td>Examiner Name</td> <td>Vo, Lilian</td> </tr> </table>		Application Number	09/823,482	Filing Date	March 30, 2001	First Named Inventor	Cota-Robles, Erik	Group Art Unit	2127	Examiner Name	Vo, Lilian
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First Named Inventor	Cota-Robles, Erik										
Group Art Unit	2127										
Examiner Name	Vo, Lilian										
<p>Sheet 1 of 1</p> <p>Attorney Docket No: 042390.P9774</p>											

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ¹	T ²
	EP-EP0473913	03/11/1992	Farrell, Joel A.			EP	EP04

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	CT 1c
		BRANDS, STEFAN , "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995),Chapter 3	
		DAVIDA, GEORGE I, et al., "Defending Systems Against Viruses through Cryptographic Authentication", <u>Proceedings of the Symposium on Security and Privacy</u> , IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989),	
		INTEL, "IA-32 Intel Architecture Software Developer's Manual", <u>Volume 3: System Programming Guide</u> , Intel Corporation - 2003,13-1 through 13-24	
		LUKE, JAHN , et al., "Replacement Strategy for Aging Avionics Computers", <u>IEEE AES Systems Magazine</u> , XP002190614,(March 1999),	
		MENEZES, OORSCHOT , "Handbook of Applied Cryptography", <u>CRC Press LLC, USA</u> XP002201307, (1997),475	
		RICHT, STEFAN , et al., "In-Circuit-Emulator Wird Echtzeittauglich", <u>Elektronik, Franzis Verlag GMBH, Munchen, DE</u> , Vol. 40, No. 16, XP000259620,(100-103),8-6-1991	
		SAEZ, SERGIO , et al., "A Hardware Scheduler for Complex Real-Time Systems", <u>Proceedings of the IEEE International Symposium on Industrial Electronics</u> , XP002190615,(July 1999),43-48	
		SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", <u>Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA</u> , (Nov. 2001),	

EXAMINER

DATE CONSIDERED

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 8-27, 2004

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Serial/Patent No.: 09/823,482 Filing/Issue Date: March 30, 2001

Client: Intel Corporation

Title: METHOD AND APPARATUS FOR CONSTRUCTING HOST PROCESSOR SOFTWARE DEVICES INDEPENDENT OF THE HOST PROCESSOR OPERATING SYSTEM

bstz File No.: 042390.P9774

Atty/Secty Initials: MGP/mb

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